

2 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Product Summary

V_{BR} (min)	I_{PP} (max)	C_T (typ)
6V	1.5A	0.5pF

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Applications

- Cellular Handsets
- Portable Electronics
- Computers and Peripheral

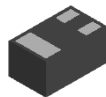
Features

- Low Profile Package (0.53mm max) and Ultra-small PCB Footprint Area (1.08 x 0.68mm max) Suitable for Compact Portable Electronics
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±15kV, Contact ±15kV
- 2 Channels of ESD Protection
- Low Channel Input Capacitance
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

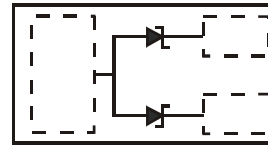
Mechanical Data

- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 ^(e4)
- Weight: 0.001 grams (approximate)

X1-DFN1006-3



Bottom View



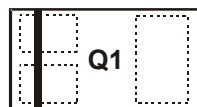
Device Schematic

Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
D5V0F2U3LP-7B	Standard	Q1	7	8	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



Top View
Bar Denotes
Cathode Side

Q19 = Product Type Marking Code

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current	I _{PP}	1.5	A	8/20μs (Note 7)
ESD Protection – Contact Discharge	V _{ESD_Contact}	±15	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V _{ESD_Air}	±15	kV	Standard IEC 61000-4-2

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	300	mW
Thermal Resistance, Junction to Ambient T _A = +25°C	R _{θJA}	417	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Standoff Voltage	V _{RWM}	—	—	5.5	V	—
Channel Leakage Current (Note 6)	I _R	—	—	100	nA	V _R = 5V, Any I/O to GND
Reverse breakdown voltage	V _{BR}	6.0	—	—	V	I _R = 1mA
Clamping Voltage, Positive Transients (Note 7)	V _C	—	10	12	V	I _{PP} = 1A, t _p = 8/20μs
Channel Input Capacitance (Note 8)	C _T	—	0.5	—	pF	V _R = 0V, f = 1MHz, Any I/O to GND
		—	0.4	0.65		V _R = 2.5V, f = 1MHz, Any I/O to GND
Dynamic Resistance	R _{DYN}	—	0.9	—	Ω	I _{PP} = 1A, t _p = 8/20μs

- Notes:
5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at <http://www.diodes.com>.
 6. Short duration pulse test used to minimize self-heating effect.
 7. Clamping voltage value is based on an 8x20μs peak pulse current (I_{pp}) waveform.
 8. Measured from any I/O to GND.
 9. For information on the impact of Diodes' USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL: http://www.diodes.com/destdtools/appnote_dnote.html.

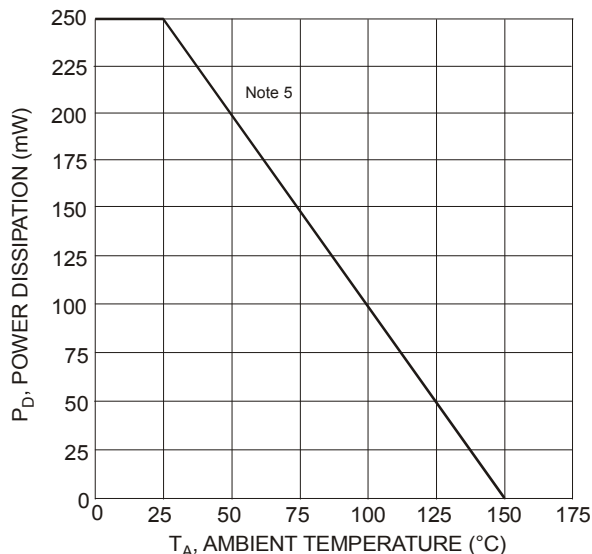


Figure 1 Power Derating Curve

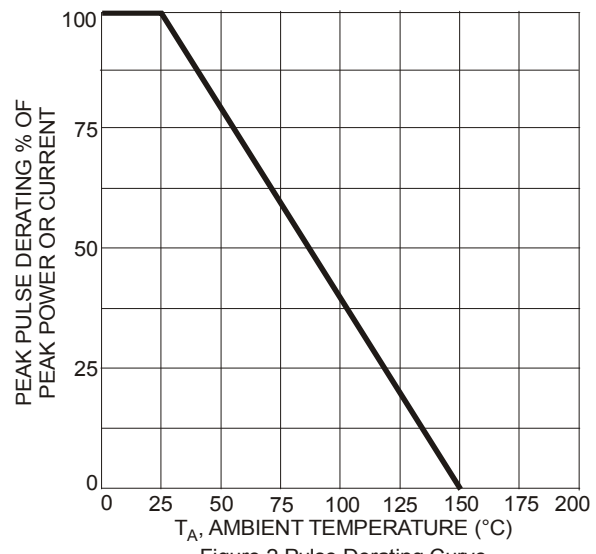


Figure 2 Pulse Derating Curve

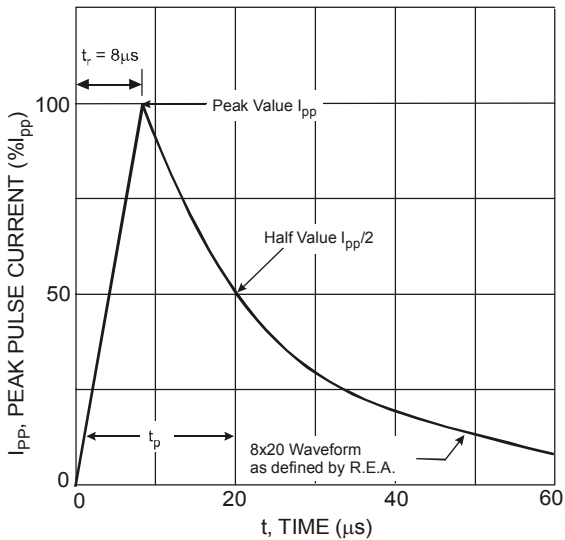


Figure 3 Pulse Waveform

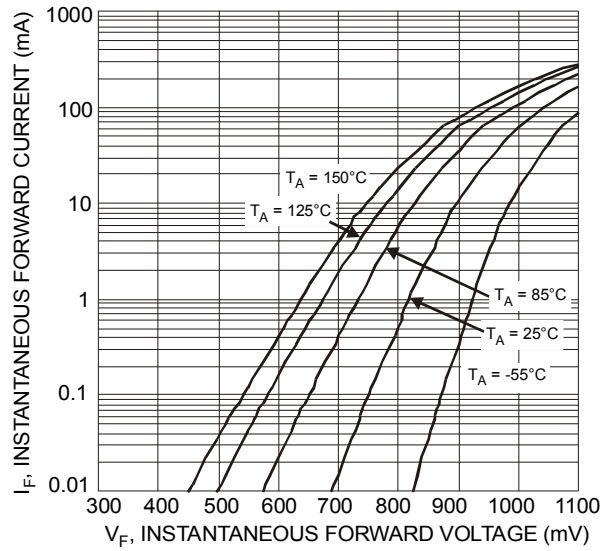


Figure 4 Typical Forward Characteristics

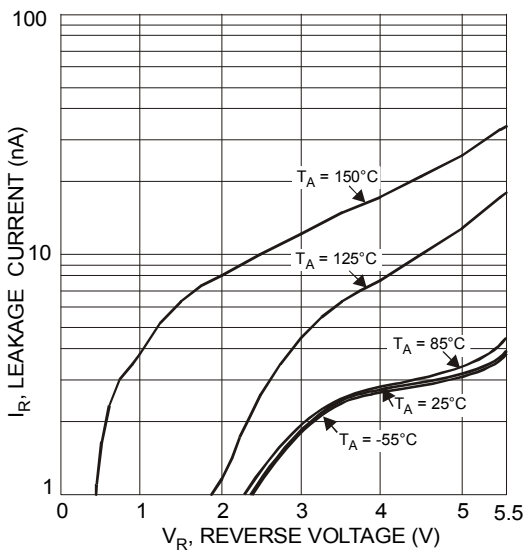


Figure 5 Typical Reverse Characteristics

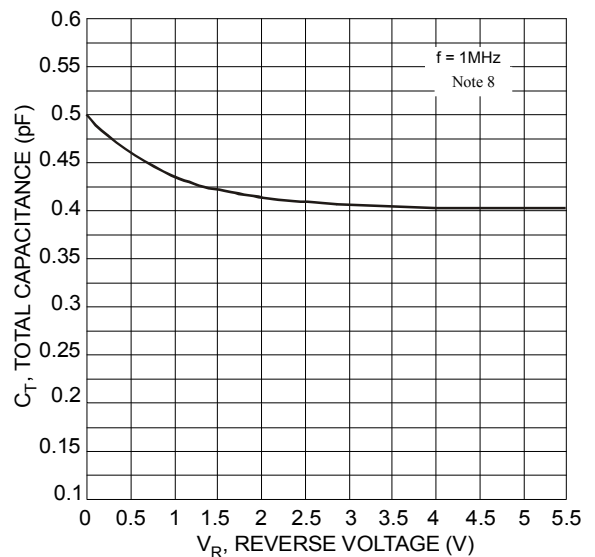
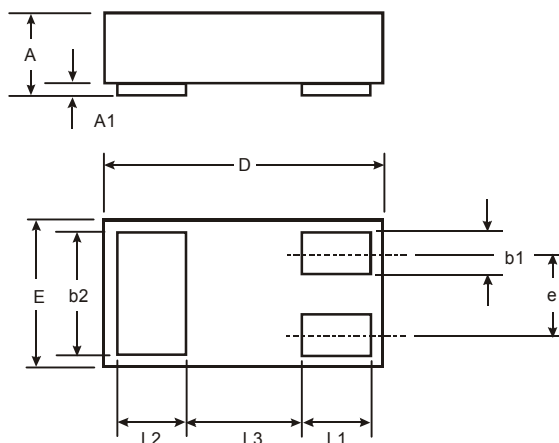


Figure 6 Total Capacitance vs. Reverse Voltage

Package Outline Dimensions

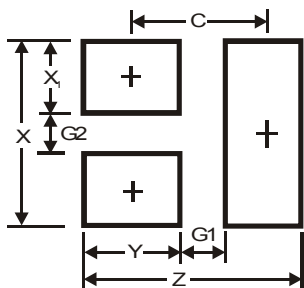
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



DFN1006-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.03
b1	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	—	—	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	—	—	0.40

All Dimensions in mm

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
X	0.7
X1	0.25
Y	0.4
C	0.7

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